
IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

First Named Inventor: Thomas J. Heller, JR. : GROUP ART UNIT 2187

Pat Appn: Serial No. 10/603252: Examiner: Jesse David Diller

Filed: June 25, 2003 : Date: July 18, 2007

Title: Multiprocessor System with Dynamic Cache Coherency Regions :

Attorney Docket: POU920030046US1 :

Deposit Account: 09-0463 : Lynn L. Augspurger, Attorney, Reg No. 24,227 :

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AMENDMENT (Responsive to OA Dated 4/20/2007)

Commissioner for Patents

Washington DC 20231

Sir:

A. Introductory Comments:

In reply to the Official action mailed 4/20/2007
Applicants respectfully request entry of the following amendment
to place the application in condition for allowance in light of
the remarks and these amendments. (The Commissioner is also
hereby authorized to charge payment of the fees associated with
this communication or credit any overpayment to Deposit Account
No. 09-0463. Those expected are already part of the separate fee
authorization.)